

*Application*  
*for*  
*United States Letters Patent*

10507 " 8462660

*To all whom it may concern:*

*Be it known that,*

*Yasunori TSUKIOKA*

*have invented certain new and useful improvements in*

METHOD AND APPARATUS FOR PERFORMING  
MULTI-FUNCTION AND EFFECTIVELY MANAGING MEMORIES

*of which the following is a full, clear and exact description:*

TITLE

METHOD AND APPARATUS FOR PERFORMING MULTI-FUNCTION AND  
EFFECTIVELY MANAGING MEMORIES

5

BACKGROUND

1. Field:

This patent specification relates to a method and  
apparatus for multi-function performance, and more  
particularly to a method and apparatus for multi-function  
10 performance capable of effectively managing memories.

2. Description of Related Art:

Conventionally, multi-function apparatuses capable of  
performing at least copying and facsimile functions are  
15 provided with a copier image memory area for a copying  
function and a facsimile image memory area for a facsimile  
function. These multi-function apparatuses often are  
capable of executing sophisticated functions such as a  
sorting copy, a memory transmission, and so on. In a  
20 typical sorting copy, for example, reading of all the  
originals is first performed. Then, the image data read  
from the respective original pages are encoded and  
compressed, and the resultant compressed image data are  
accumulated into the copying image memory area. After  
25 that, the image data are reproduced according to a

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designated number of reproductions which are collated and  
ejected into sorting trays. In a typical memory  
transmission, for another example, data of image  
information to be transmitted or which have been received  
5 are accumulated into the facsimile image memory area during  
a broadcasting transmission, a confidential transmission,  
or a proxy receiving when a memory transmission mode is  
selected.

In the above-described sorting copy, a maximum number  
10 of reproducible originals in a single task of the sorting  
copy depends mostly on a data capacity of the copying image  
memory area although it also depends on complexity of the  
images contained in the originals. This situation would be  
applied also to the memory transmission. That is, a  
15 maximum number of transmittable originals in a single task  
of the memory transmission depends mostly on a data  
capacity of the facsimile image memory area.

Conventionally, a multi-function apparatus is  
configured to have an add-on expansion memory so as to  
20 increase a maximum page number of reproducible originals in  
the sorting copy. In this case, however, the expansion  
memory is used only for the copying function. To increase  
the data capacities of both copying and facsimile image  
memory areas, two separate expansion memories are required,

which would lead to an increase of an apparatus cost.

#### SUMMARY

5 This patent specification describes a novel multi-  
function apparatus performing at least copying and  
facsimile functions. In one example, a novel multi-  
function apparatus includes a basic memory, a socket, and a  
controller. The basic memory includes at least a first  
10 image memory area for storing data of the copying function  
and a second image memory area for storing data of the  
facsimile function. The socket connects an add-on  
expansion memory. The controller is configured to arrange  
memory assignments of the basic memory in accordance with a  
first memory map to effectively perform operations of the  
15 copying and facsimile functions when the expansion memory  
is not mounted to the socket. The controller is also  
configured to rearrange the memory assignments of the basic  
memory together with the expansion memory with an effect of  
expanding the first and second image memory areas of the  
20 basic memory in accordance with a second memory map to  
effectively perform the operations of the copying and  
facsimile functions when the expansion memory is mounted to  
the socket.

The above-described multi-function apparatus may further include a backup battery that energizes the basic memory while other power to the apparatus is turned off. In this case, the second image memory area is allocated within the basic memory when the expansion memory is mounted to the socket.

The controller may return an arrangement of the memory assignments from that in accordance with the second memory map to the other in accordance with the first memory map when the expansion memory is removed from the socket.

The controller may indicate a user guidance providing a notice that the expansion memory is installed to the socket while the second image memory area retains effective image data therein.

The controller may indicate a user guidance providing a notice that the expansion memory is removed from the socket while the second image memory area retains effective image data therein.

Further, this patent specification describes another novel multi-function apparatus that performs at least a copying function and an optional facsimile function. In one example, this multi-function apparatus includes a basic memory, a socket, and a controller. The basic memory includes at least a first image memory area for storing

data of the copying function. The socket connects an add-on expansion memory. The controller is configured to arrange memory assignments of the basic memory and the expansion memory in accordance with a predetermined memory map so that a second image memory area for the optional facsimile function is allocated within the basic memory when the expansion memory is mounted to the socket and the optional facsimile function is added to the apparatus.

The above-described multi-function apparatus may further include a backup battery that backs up the basic memory.

The second image memory area has a memory size which may be determined by a user.

Further, this patent specification describes a novel method for multi-function performance of at least copying and facsimile functions. In one example, a novel method for multi-function performance includes the steps of providing, preparing, arranging, and rearranging. The providing step provides a basic memory including a first memory area for storing data of the copying function and a second memory area for storing data of the facsimile function. The preparing step prepares a socket for connecting an expansion memory. The arranging step arranges memory assignments of the basic memory in

accordance with a first memory map to effectively perform operations of the copying and facsimile functions when the expansion memory is not mounted to the socket. The rearranging step rearranges the memory assignments of the basic memory together with the expansion memory with an effect of expanding the first and second memory areas storing the data of the copying and of facsimile functions in accordance with a second memory map to effectively perform the operations of the copying and facsimile functions when the expansion memory is mounted to the socket.

The above-described method may further include a step of energizing at least the basic memory while other power to the multi-function performance is turned off.

The above-described method may further include a step of returning an arrangement of the memory assignments from that in accordance with the second memory map to the other in accordance with the first memory map when the expansion memory is removed from the socket.

The above-described method may further include a step of providing a user guidance with a notice that the expansion memory is installed to the socket while the second memory area retains effective image data therein.

The above-described method may further include a step of providing a user guidance with a notice that the expansion memory is removed from the socket while the second memory area retains effective image data therein.

5 Further, this patent specification describes another method for multi-function performance of at least a copying function and an optional facsimile function. In one example, a method for multi-function performance includes the steps of providing, preparing, and arranging. The  
10 providing step provides a basic memory for storing data of the copying function. The preparing step prepares a socket for connecting an add-on expansion memory. The arranging step arranges memory assignments of memory areas of the basic memory and the expansion memory in accordance with a  
15 predetermined memory map so that a specific memory area for the optional facsimile function is allocated within the basic memory when the expansion memory is mounted to the socket and the optional facsimile function is made available.

20 The above-described method may further include a step of energizing at least the basic memory while other power to the multi-function performance is turned off.

The specific memory area has a memory area which may be determined by a user.



## BRIEF DESCRIPTION OF THE DRAWINGS

A more complete appreciation of the disclosure and many of the attendant advantages thereof will be readily obtained as the same becomes better understood by reference to the following detailed description when considered in connection with the accompanying drawings, wherein:

Fig. 1 is a schematic diagram of a multi-function apparatus according to a preferred embodiment;

Fig. 2 is an illustration for explaining an exemplary memory map of a RAM (random access memory) when an add-on optional expansion memory is not mounted to the multi-function apparatus of Fig. 1;

Fig. 3 is an illustration for explaining an exemplary memory map of the RAM and the add-on optional expansion memory mounted to the multi-function apparatus of Fig. 1;

Figs. 4A and 4B are flowcharts for explaining an exemplary procedure of an initialization process performed by the multi-function apparatus of Fig. 1;

Fig. 5 is a schematic diagram of a multi-function apparatus according to another preferred embodiment;

Fig. 6 is an illustration for explaining an exemplary memory map of a RAM (random access memory) when the add-on optional expansion memory is not mounted to the multi-function apparatus of Fig. 5;

Fig. 7 is an illustration for explaining an exemplary memory map of the RAM and the add-on optional expansion memory mounted to the multi-function apparatus of Fig. 5;

Fig. 8 is an illustration for explaining an exemplary memory map of the RAM and the add-on optional expansion memory when an option facsimile unit is mounted to the multi-function apparatus of Fig. 5;

Fig. 9 is an illustration for explaining another exemplary memory map of the RAM and the add-on optional expansion memory when an option facsimile unit is mounted to the multi-function apparatus of Fig. 5; and

Fig. 10 is a flowchart for explaining an exemplary procedure of an initialization process performed by the multi-function apparatus of Fig. 5.

#### DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

In describing preferred embodiments illustrated in the drawings, specific terminology is employed for the sake of clarity. However, the disclosure of this patent specification is not intended to be limited to the specific terminology so selected and it is to be understood that each specific element includes all technical equivalents which operate in a similar manner.

Referring now to the drawings, wherein like reference numerals designate identical or corresponding parts throughout the several views, and more particularly to Fig. 1 thereof, a multi-function apparatus 100 according to a preferred embodiment is explained. The multi-function apparatus 100 is provided at least with a digital copying function and a Group-III facsimile (G3 FAX) function. As shown in Fig. 1, the multi-function apparatus 100 includes a CPU (central processing unit) 1, a ROM (read only memory) 2, a RAM (random access memory) 3, a battery 4, a clock circuit 5, a scanner 6, and a printer 7. The multi-function apparatus 100 further includes a console unit 8, an encoder/decoder 9, a G3 FAX modem 10, a network controller 11, a socket 12, an expansion memory 13, and an internal bus 14.

The CPU 1 is employed as a controller for controlling respective components of the multi-function apparatus 100 to perform a predetermined copying function and a predetermined G3 FAX facsimile function. The ROM 2 stores programs (i.e., an operation system, applications programs, etc.) executed by the CPU 1, a variety of data required for the CPU 1 to execute the programs, and so on. The RAM 3 is employed as a basic memory and has a memory capacity of 8 MB (megabyte), for example. The RAM 3

basically includes three memory areas by functional allocation as and when needed; a working memory area for the CPU 1 to use, a copying image memory area, and a facsimile image memory area. In addition, the RAM 3 stores  
5 a variety of information inherent to the multi-function apparatus 100. The battery 4 backs up the RAM 3. The clock circuit 5 calculates and outputs current time information.

The scanner 6 reads an original image at a  
10 predetermined resolution. The printer 7 records and outputs an image at a predetermined resolution. The console unit 8 includes a variety of operation keys (not shown) and a variety of indicators (not shown) which are provided for a user to operate the multi-function apparatus  
15 100. The encoder/decoder 9 encodes, or compresses, image data and decodes encoded, or compressed, image information to original image data.

The G3 FAX modem 10 implements a modem functionality with respect to the Group-III facsimile standard and has a  
20 lower speed model function (i.e., a V.21 modem) for communicating transmission procedure signals and a higher speed model function (i.e., a V.17 modem, a V.34 modem, a V.29 modem, a V.27ter Modem, or the like) for mainly communicating image information. The network controller 11

is provided to connect the multi-function apparatus 100 to an analog public switched telephone network (PSTN) and has an automatic call-initiation and call-receiving function.

The socket 12 electrically connects an optional add-on

5 expansion memory card (i.e., the expansion memory 13) to the multi-function apparatus 100. The expansion memory 13 contains 4-MB memory banks 21 - 28, for example, which has a memory capacity of 32 MB in total, and it can be installed to and removed from the socket 12, by a user's  
10 choice.

The internal bus 14 interconnects the CPU 1, the ROM 2, the RAM 3, the clock circuit 5, the scanner 6, the printer 7, the console unit 8, the encoder/decoder 9, the G3 FAX modem 10, the network controller 11, and the socket  
15 12 so that these components perform communications of data with each other mainly through the internal bus 14. On the other hand, the network controller 11 and the G3 FAX modem 10 perform communications of data directly therebetween.

The expansion memory 13 is also interconnected to the

20 internal bus 14 via the socket 12 so that the CPU 1 is able to use the expansion memory 13 when necessary.

In the present preferred embodiment, the working memory area for the CPU 1 is differently assigned in the RAM 3 between the cases when the expansion memory 13 is

employed in the multi-function apparatus 100 and when it is not. Fig. 2 illustrates an exemplary memory map of the RAM 3 which shows memory assignments of the RAM 3 when the expansion memory 13 is not employed in the multi-function apparatus 100. The RAM 3 is divided by functional allocation into memory areas R1, R2, and R3, as shown in Fig. 2. The memory area R1 stores image data (i.e., image information) associated with the facsimile functions. The memory area R1 may be referred to as a SAF (stored and forward) memory. The memory area R2 stores a page of image data which will be recorded on the printer 7, and may be referred to as a page memory. The memory area R3 is reserved as a working memory for the CPU 1. In the RAM 3 having the memory capacity of 8 MB, memory sizes of 1 MB, 5.3 MB, and 1.7 MB maybe assigned to the memory areas R1, R2, R3, respectively, for example.

Fig. 3 illustrates an exemplary memory map of the RAM 3 added with the expansion memory 13, which shows memory assignments by functional allocation of the RAM 3 and the expansion memory 13 when the expansion memory 13 is installed in the multi-function apparatus 100. The RAM 3 is divided by functional allocation into memory areas R1-1, R3, R1-2, and R2-1 such that the memory areas R1-1, R3, R1-2, and R2-1 have, for example, 1 MB, 1.4 MB, 3 MB, and 2.6

MB, respectively. The memory areas R1-1 and R1-2 store  
facsimile image information. The memory area R2-1 stores a  
page of image data which will be recorded on the printer 7.  
The memory area R3 is used as a working memory area by the  
5 CPU 1.

The expansion memory 13 illustrated in Fig. 3  
includes 4-MB memory banks 21 - 28, for example, which has  
the memory capacity of 32 MB in total and in which a data  
access is carried out, for example, with a bank mapping  
10 operation. The memory bank 21 is divided by functional  
allocation into a memory area R2-2 having, for example, 2.7  
MB for use in the copying function and a memory area R4-1  
having, for example, 1.3 MB for accumulating pages of  
images for the copying function. The memory bank 22 is  
15 assigned as a memory area R4-2 for accumulating pages of  
images. The remaining memory banks 23 - 28 having 24 MB in  
total are assigned as memory areas R5-1, R5-2, R5-3, R5-4,  
R5-5, and R5-6, respectively, which are used for the  
copying function in a sorting mode.

20 Consequently, in the present embodiment, the memory  
area R1 for storing facsimile images is composed of the  
functionally allocated memory areas R1-1 and R1-2 and has a  
memory capacity of 4 MB in total, which is 3 MB greater  
than the memory capacity of the memory area R1 of Fig. 2.

As shown in Fig. 3, the memory areas R1-1 and R1-2 are located in the RAM 3 backed up with the battery 4 and therefore the accumulated image data associated with the facsimile function are safely retained even when other power to the apparatus is turned off. With this configuration, the multi-function apparatus 100 can properly perform special facsimile functions such as a facsimile transmission with time designation, a facsimile transmission with confidentiality, and a proxy facsimile receiving.

Furthermore, in the present preferred embodiment, the multi-function apparatus 100 handles the memory areas R2-1 and R2-2 as one memory area R2 and allows data access to such memory area R2. Likewise, the memory areas R4-1 and R4-2 are handled as one memory area R4 and, also, the memory areas R-1, R-2, R-3, R-4, R-5, and R-6 are handled as one memory area R5.

In this embodiment, the CPU 1 uses the memory area R4 together with the memory area R2 as double buffers for the copying function. This enables the CPU 1 to perform operations of reading and storing image data from an original in parallel to operations of reading and reproducing image data from the memory, thereby increasing a processing speed of the copying operation. The CPU 1



uses the memory area 5 for the copying function in the  
sorting mode. For example, during the copying function in  
the sorting mode, the CPU 1 stores data read from originals  
into the memory area 5 after encoding and compressing the  
5 read data.

It should be noted that the power to the multi-  
function apparatus 100 should be turned off when the above-  
described expansion memory 13 is installed to or removed  
from the multi-function apparatus 100. Accordingly, the  
10 CPU 1 should check presence of the expansion memory 13  
during an initialization operation at a power-up time.

Referring to Figs. 4A and 4B, an exemplary procedure  
of an initialization operation performed by the multi-  
function apparatus 100 is explained. As illustrated in  
15 Fig. 4A, when the initialization operation is started, the  
CPU 1 obtains memory configuration information in Step S101  
and checks whether or not the expansion memory 13 is  
actually installed in Step S102. If the check result of  
Step S102 is YES, the CPU 1 checks in Step S103 whether the  
20 memory configuration information is updated and properly  
corresponds to an actual configuration in that the  
expansion memory 13 is installed as checked out in Step  
S102. If the check result of Step S103 is NO, the CPU 1  
determines that the memory configuration information is not

updated in accordance with the actual configuration and stops the process.

If the memory configuration information is updated and the check result of Step S103 is YES, the process

5 proceeds to Step S104. The CPU 1 checks in Step S104 if the SAF memory (i.e., the memory area R1) reserved in the RAM 3 stores effective image data. If the check result of Step S104 is YES, the CPU 1 instructs the console unit 8 in Step S105 to provide notice that unknown image data remains  
10 in the SAF memory. Then, in Step S106, the CPU 1 checks whether in response to the warning notice a user inputs an instruction for saving or deleting the unknown image data remaining in the SAF memory. When the user inputs a saving instruction and the check result of Step S106 is "SAVE,"  
15 the CPU 1 instructs the console unit 8 in Step S107 to keep on indicating the warning and ends the process.

When the user inputs a deleting instruction and the check result of Step S106 is "DELETE," the CPU 1 proceeds to Step S108 in which the CPU 1 initializes a memory

20 management under conditions that the expansion memory 13 is installed, as illustrated in Fig. 3. Subsequently, in Step S109, the CPU 1 updates the memory configuration information into conditions that the expansion memory 13 is installed, as illustrated in Fig. 3. Then, the CPU 1 ends

the process. Likewise, when effective image data is not stored in the SAF memory of the RAM 3 and the check result of Step S104 is NO, the CPU 1 also performs the processes of Steps S108 and S109 and then ends the process.

5           If the expansion memory 13 is not actually installed and the check result of Step S102 is NO, the process proceeds to Step S110 of Fig. 4B. In Step S110, the CPU 1 checks whether the memory configuration information is updated and properly corresponds to an actual configuration  
10 in that the expansion memory 13 is not installed as checked out in Step S102. If the check result of Step S110 is NO, the CPU 1 determines that the memory configuration information is not updated in accordance with the actual configuration and stops the process.

15           If the memory configuration information is updated and the check result of Step S110 is YES, the process proceeds to Step S111. The CPU 1 checks in Step S111 if the SAF memory (i.e., the memory area R1) reserved in the RAM 3 stores effective image data. If the check result of  
20 Step S111 is YES, the CPU 1 instructs the console unit 8 in Step S112 to provide a warning notice that unknown image data remains in the SAF memory. Then, in Step S113, the CPU 1 checks whether in response to the warning a user inputs an instruction for saving or deleting the unknown

image data remaining in the SAF memory. When the user  
inputs a saving instruction and the check result of Step  
S113 is "SAVE," the CPU 1 instructs the console unit 8 in  
Step S114 to keep on indicating the warning and ends the  
5 process.

When the user inputs a deleting instruction and the  
check result of Step S113 is "DELETE," the CPU 1 proceeds  
to Step S115 in which the CPU 1 initializes a memory  
management under conditions that the expansion memory 13 is  
10 installed, as illustrated in Fig. 3. Subsequently, the  
process proceeds to Step S109 (Fig. 4A) in which the CPU 1  
updates the memory configuration information into  
conditions that the expansion memory 13 is not installed,  
as illustrated in Fig. 2. Then, the CPU 1 ends the  
15 process. Likewise, when effective image data is not stored  
in the SAF memory of the RAM 3 and the check result of Step  
S111 is NO, the CPU 1 also performs the processes of Steps  
S115 and S109 (Fig. 4A) and then ends the process.

In this way, the multi-function apparatus 100  
20 provides a warning indication to the user in an event that  
an effective image information file is retained in the SAF  
memory (i.e., the memory area R1) after the expansion  
memory 13 is installed or removed. Thereby, the user can  
appropriately perform the installation or removal of the

expansion memory 13.

It should be noted that the memory capacity of the expansion memory 13 may not be limited to 32 MB which is used as an exemplary memory capacity in the foregoing description. Also, it should be noted that the entire page memory (i.e., the memory area R2) may be allocated in the expansion memory 13 since the data stored in the page memory may usually not be used when the power to the multi-function apparatus 100 is turned off although in the foregoing description the page memory is partly allocated in the RAM 3 in a memory map with the expansion memory 13 installed.

Furthermore, it should be noted that the multi-function apparatus 100 may include a Group IV facsimile function as well as the copying function with or without a printer function although the multi-function apparatus 100 includes the Group III facsimile function and the copying function in the foregoing description.

Referring to Fig. 5, a multi-function apparatus 200 according to another embodiment is explained. The multi-function apparatus 200 is similar to the multi-function apparatus 100 illustrated in Fig. 1, except for an optional facsimile unit 30. The multi-function apparatus 200 is configured to perform at least a copying function and, as

an option, a Group-III facsimile (G3 FAX) function when the optional facsimile unit 30 is installed by a user choice to the multi-function apparatus 200. The option facsimile unit 30 includes the G3 FAX modem 10 and the network controller 11. The CPU 1 controls respective components of the multi-function apparatus 200 to perform a predetermined copying function control. The CPU 1 also performs a predetermined G3 FAX facsimile operation when the option facsimile unit 30 is optionally installed, as described above. The G3 FAX modem and the network controller 10 of the FAX unit 30 are interconnected to each other for a direct communications therebetween. When the FAX unit 30 is installed in the multi-function apparatus 200, the G3 FAX modem and the network controller 10 are interconnected to the internal bus 14 to communicate with other components.

Fig. 6 illustrates an exemplary memory map of the RAM 3 which shows functional allocation memory assignments of the RAM 3 when the expansion memory 13 is not mounted to the multi-function apparatus 200. In Fig. 6, the RAM 3 is divided by functional allocation when and as needed into memory areas R11, R12, and R13 in a manner similar to that shown in Fig. 2. The memory area R11 stores image data (i.e., image information) associated with the facsimile

functions, and it may be referred to as a SAF (stored and forward) memory. The memory area R12 stores a page of image data which will be recorded on the printer 6, and therefore it may be referred to as a page memory. The

5 memory area R13 is reserved as a working memory for the CPU 1. In this case, the RAM 3 has a memory capacity of 8 MB (megabyte) and the memory areas R11, R12, R13 have 1 MB, 5.3 MB, and 1.7 MB, respectively, for example.

Although the memory map of Fig. 6 includes the memory 10 area R11 reserved for the facsimile function, such memory are R11 may be used for other functions such as the copying function, for example, when the FAX unit 30 is not installed.

Fig. 7 illustrates an exemplary memory map of the RAM 15 3 added with the expansion memory 13, which shows memory assignments of the RAM 3 and the expansion memory 13 when the expansion memory 13 is mounted to the multi-function apparatus 200 that has no FAX unit 30 installed therein. The RAM 3 is divided by functional allocation into the 20 memory areas R11, R12, and R13, in a manner similar to that of Fig. 6. The memory sizes, for example, of 1 MB, 5.3 MB, and 1.7 MB are allocated to the memory areas R11, R12, and R13, respectively. In the example of a 32-MB expansion memory 13 composed of 4-MB memory banks 21 - 28, the memory

bank 21 is assigned as a memory area R14-1 serving as a 4-MB page memory for the copying function. The memory bank 22 is divided into a memory area R14-2, having 1.3 MB, for accumulating page images for the copying function and a  
5 memory area 15-1, having 2.7 MB, for the copying function in a sorting mode. The remaining memory banks 23 - 28 having 24 MB in total are assigned as memory areas R15-2, R15-3, R15-4, R15-5, R15-6, and R15-7, respectively, which are used for the copying function in the sorting mode.

10 Although the memory map of Fig. 7 includes the memory area R11 reserved for the facsimile function, such memory are R11 may be used for other functions such as the copying function, for example, when the FAX unit 30 is not installed.

15 The FAX unit 30 may be installed by a user choice to the multi-function apparatus 200 in which the RAM 3 and the expansion memory 13 have the above-described memory assignments. Upon an installation of the FAX unit 30, the multi-function apparatus 200 changes the memory assignments  
20 of the RAM 3 and the expansion memory 13 into those of a memory map shown in Fig. 8 which are similar to the memory assignments shown in Fig. 3, in which a 4-MB memory is assigned as the SAF memory.



In the memory map shown in Fig. 8, the RAM 3 is divided by functional allocation into the memory areas R11-1, R13, R11-2, and R12-1 having the memory sizes of, for example, 1 MB, 1.4 MB, 3 MB, and 2.6 MB, respectively. The memory areas R11-1 and R11-2, having 4 MB in total, serve as a storage of facsimile image information. The memory area R12-1 serves as a page memory for the copying function. The memory area R13 is served as a working memory area for the CPU 1. In the expansion memory 13, the memory bank 21 is divided by functional allocation into a memory area R12-2 having, for example, 2.7 MB for use in the copying function and a memory area R14-1 having, for example, 1.3 MB for accumulating page images for the copying function. The memory bank 22 is assigned as a memory area R14-2 having 4 MB for accumulating page images for the copying function. The remaining memory banks 23 - 28 having 24 MB in total are assigned as memory areas R15-1, R15-2, R15-3, R15-4, R15-5, and R15-6, respectively, which are used for the copying function in a sorting mode.

The memory area R11 having 4 MB in the above-described configuration may be arranged to have a different memory size. Fig. 9 illustrates another exemplary memory map of the RAM 3 and the expansion memory 13, which shows memory assignments that assign 6 MB to the memory area R11.

In this case, the memory areas R11-1, R13, R11-2, and R12-1 of the RAM 3 have, for example, 1 MB, 1.4 MB, 5 MB, and 0.6 MB, respectively. Thus, the memory area R11 composed of the memory areas R11-1 and R11-2 has 6 MB in total. In the expansion memory 13, the memory bank 21 is allocated as the memory area R12-2 having 4 MB for use in the copying function. The memory bank 22 is divided, for example, into the memory area R12-3 having 0.7 MB as the page memory for the copying function and the memory area R14-1 having 3.3 MB as a page memory for accumulating page images for the copying function. The memory bank 23 is divided into the memory area 14-2 having 2 MB as another page memory for accumulating page images for the copying function and the memory area 15-1 having 2 MB for the copying function in the sorting mode. The remaining memory banks 24 - 28 having 20 MB in total are assigned as the memory areas R15-2, R15-3, R15-4, R15-5, and R15-6, respectively, and are used for the copying function in the sorting mode.

The multi-function apparatus 200 having the expansion memory 13 and the FAX unit 30 may thus expand the memory area R1 (i.e., the SAF memory) to 6 MB, thereby performing versatile functions based on the facsimile functions. In addition, the memory area R1 composed of the memory areas R1-1 and R1-2 is located in the RAM 3 backed up with the

battery 4 and therefore the accumulated image data associated with the facsimile function are safely retained even when other power to the apparatus is turned off. With this configuration, the multi-function apparatus 200 can properly perform special facsimile functions such as a facsimile transmission with time designation, a facsimile transmission with confidentiality, and a proxy facsimile receiving.

It should be noted that the power to the multi-function apparatus 200 should be turned off when the above-described expansion memory 13 is installed to or removed from the multi-function apparatus 200. Accordingly, the CPU 1 should check for presence of the expansion memory 13 during an initialization operation at a power-up time.

Referring to Fig. 10, an exemplary procedure of the initialization operation performed by the multi-function apparatus 200 is explained. As illustrated in Fig. 10, when the initialization operation is started, the CPU 1 checks in Step S201 whether the expansion memory 13 is mounted to the multi-function apparatus 200. If the check result of Step S201 is YES, the CPU 1 then checks in Step S202 whether the FAX unit 30 is installed in the multi-function apparatus 200. If the check result of Step S202 is YES, the CPU 1 further checks in Step S203 whether a

memory allocation has been carried out on the SAF memory  
(i.e., the memory area R1).

If the check result of Step S203 is NO, the CPU 1  
proceeds to Step S204 to instruct the console 8 to provide  
5 user guidance indicating that the user is required to  
determine a memory size of the SAF memory. If the check  
result of Step S203 is YES, the CPU 1 skips the process of  
Step S204. Upon a determination of the SAF memory size by  
the user, the CPU 1 initializes the memory management in  
10 Step S205 in accordance with the determination of the SAF  
memory size by the user and under the conditions that the  
expansion memory 13 is mounted to the multi-function  
apparatus 200. Then, the CPU 1 proceeds to Step S206 in  
which the CPU 1 updates the memory configuration  
15 information to the conditions that the expansion memory 13  
is mounted, as illustrated in Fig. 8 or Fig. 9. The  
process then ends.

When the expansion memory 13 is attached but the FAX  
unit 30 is not, the check result of Step S202 will be NO  
20 and the CPU 1 will proceed to Step S207. In Step S207, the  
CPU 1 allocates the entire memory capacity of the expansion  
memory 13 to the memory areas R14 and R15 for the use of  
the copying function, as shown in Fig. 7, and initializes  
the memory configuration information accordingly. Then,

the CPU 1 proceeds to Step S206 to update the memory configuration information to the conditions in that the expansion memory 13 is mounted, as described above. The CPU 1 then ends the process.

5           When the expansion memory 13 is not mounted and the check result of Step S201 is NO, the CPU 1 proceeds to Step S208. In Step S208, the CPU 1 allocates the memory capacity of the RAM 3 in the manner as shown in Fig. 6, and initializes the memory configuration information  
10 accordingly. Then, the CPU 1 proceeds to Step S206 to update the memory configuration information to the conditions in that the expansion memory 13 is not mounted, as described above. The CPU 1 then ends the process.

          The disclosure of this patent specification may be  
15 conveniently implemented using a conventional general purpose digital computer programmed according to the teaching of the present specification, as will be apparent to those skilled in the computer art. Appropriate software coding can readily be prepared by skilled programmers based  
20 on the teachings of the present disclosure, as will be apparent to those skilled in the software art. The disclosure of this patent specification may also be implemented by the preparation of application specific integrated circuits or by interconnecting an appropriate

network of conventional component circuits, as will be readily apparent to those skilled in the art. When reference is made to dividing memory into areas, it should be clear that this includes functional allocation in which the system keeps track of address for respective types of information, without necessarily using physically separate memory areas.

Numerous additional modifications and variations are possible in light of the above teachings. It is therefore to be understood that within the scope of the appended claims, the disclosure of this patent specification may be practiced otherwise than as specifically described herein.

This application is based on Japanese patent applications, No. JPAP2000-343855 filed on November 11, 2000 and No. JPAP2001-089835 filed on March 27, 2001 in the Japanese Patent Office, the entire contents of which are incorporated by reference herein.